## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

application of:

Mike A. Hergott

09/821,226

Art Unit:

2133

Filed:

March 28, 2001

Examiner:

Dooley, M.

For:

JTAG TEST ACCESS PORT CONTROLLER USED TO CONTROL

INPUT/OUTPUT PAD FUNCTIONALITY

**Commissioner for Patents** P.O. Box 1450 Alexandria, VA 22313-1450

Technology Center 2100

## **SUBMISSION OF DRAWING AMENDMENT (37 CFR 1.121)**

Attached please find

(check applicable items)

replacement drawing sheet(s) in permanent ink [X]

annotated marked-up copy of the amended drawing(s) with annotations showing [ ] the change(s).

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Dated: July 29, 2004

## **CERTIFICATE OF MAILING (37 CFR 1.8(a))**

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Jan M. Dunbar

Date: July 29, 2004

Applic. No. 09/821, 226
Reply to Office Action of 06/03/2004
Replacement Sheet AUG 0 2 2004 -100 110 106 B/S **CELL** 110 114 SYSTEM CORE LOGIC B/S CELL EN 102-B/S **CELL** DATA CNT 118 112 110 B/S CELL 110 110 EN B/S CELL B/S CELL DATA 102 -CNT iz 118 108 TRST TDO TAP CONTROLLER TDI **TMS** TCK

FIG. 1 (CONVENTIONAL)